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	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01207M	

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/JP/	1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	<input type="checkbox"/>
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/JP/	23	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 14, 2005 from http://www.algonet.se/~staffann/developer/emulator.htm ; 6 pages 11/8/2007	<input type="checkbox"/>
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